

# Design and Analysis of Discrete Current Regulators for VSIs



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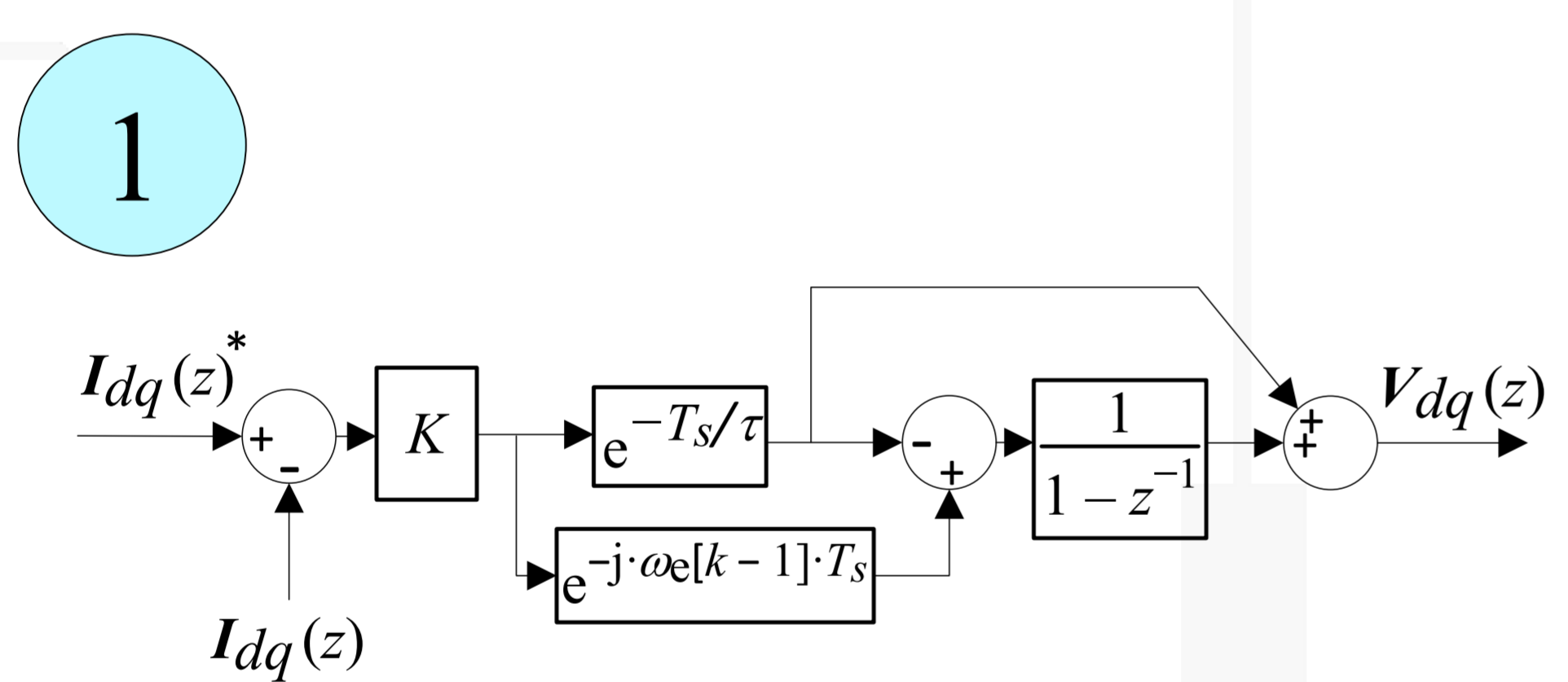


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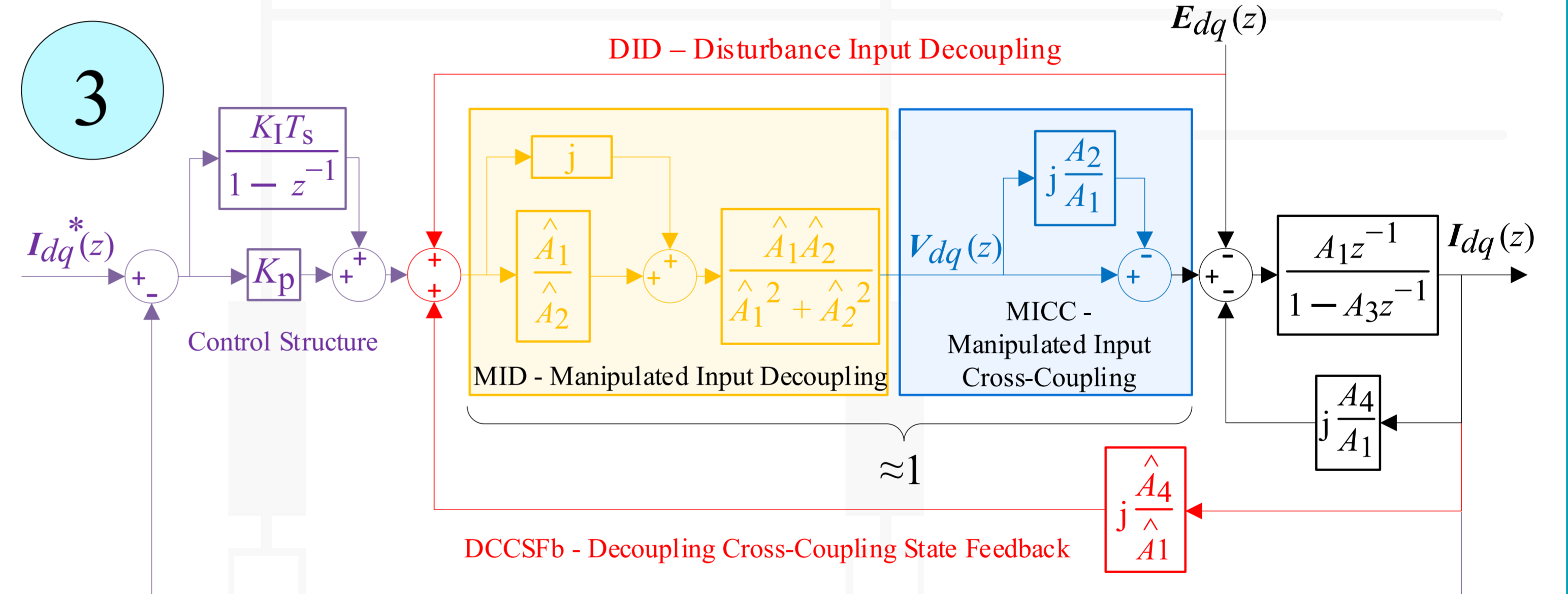
## Introduction

- This paper compares the quasi-continuous dq-decoupling techniques to various discrete modeling approaches for *Voltage Oriented Control* (VOC)
- To analyze the different techniques, this paper provides both *Frequency Response Functions* (FRFs) for *Command Tracking* (CT) and *Dynamic Stiffness* (DS), and time domain tests
- Two *Voltage Source Inverter* (VSI) topologies have been used:
  1. Small-Scale Laboratory HVDC-MMC
  2. Industrial Converter of a 3 MW Wind Turbine Generator

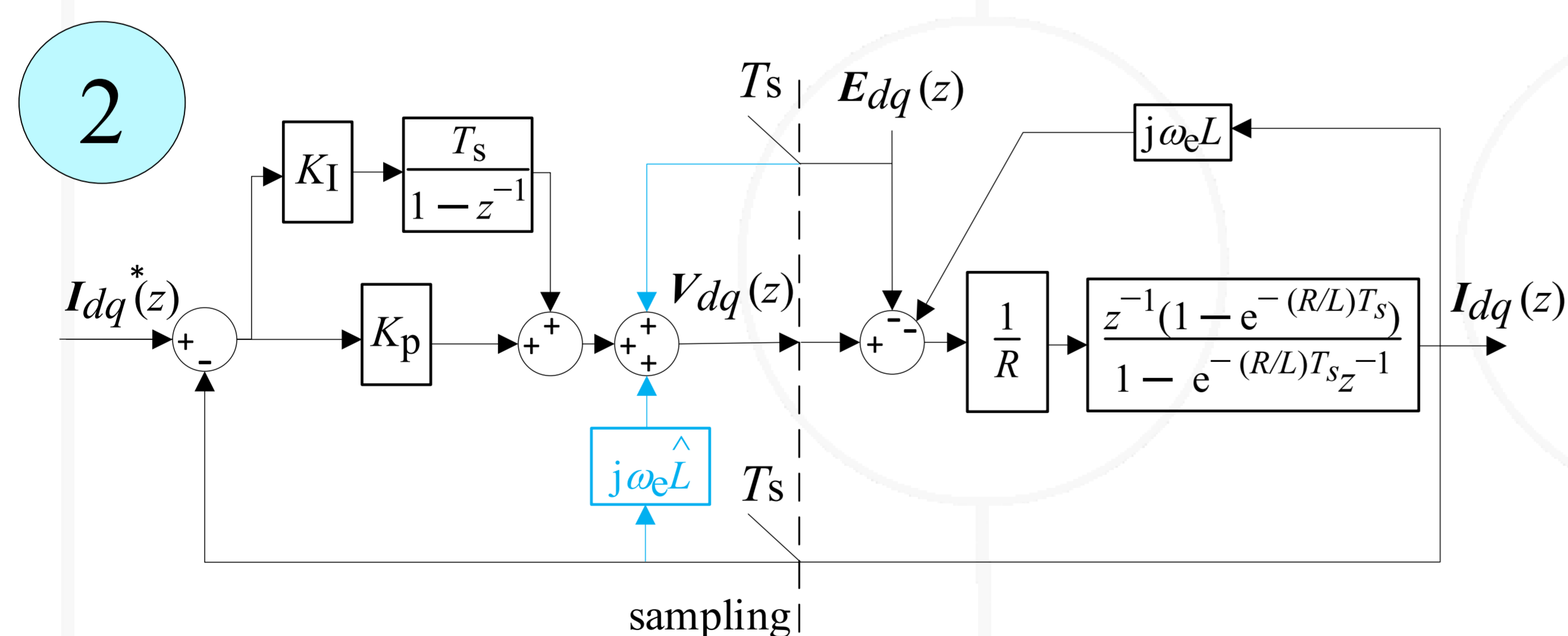
## Compared dq-Decoupling Techniques



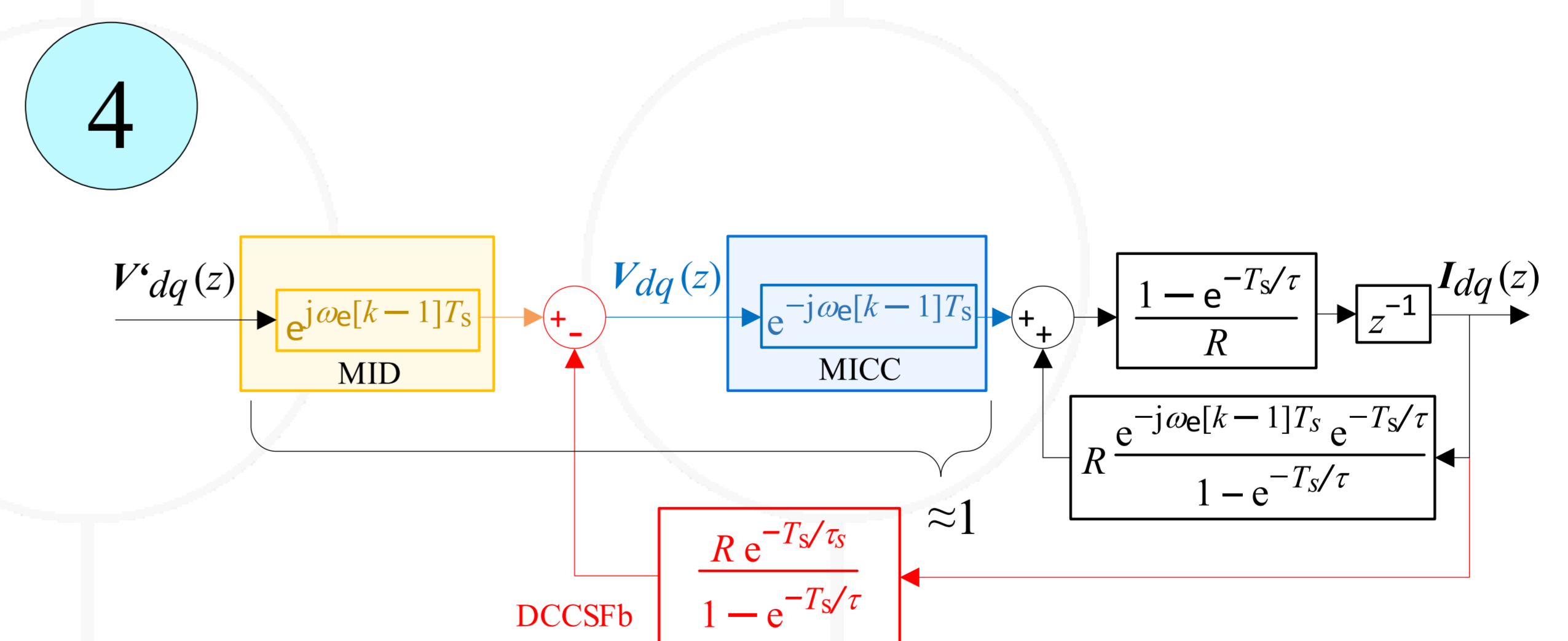
Discrete complex vector synchronous frame PI current regulator obtained via direct modeling (Briz, et.al, 2000).



Proposed discrete-time complex vector synchronous frame PI current regulator with discrete DCCSFb and DID (red), MICC (blue), and MID (yellow).



Discrete-Time PI controller with DID and continuous DCCSFb approach in blue.

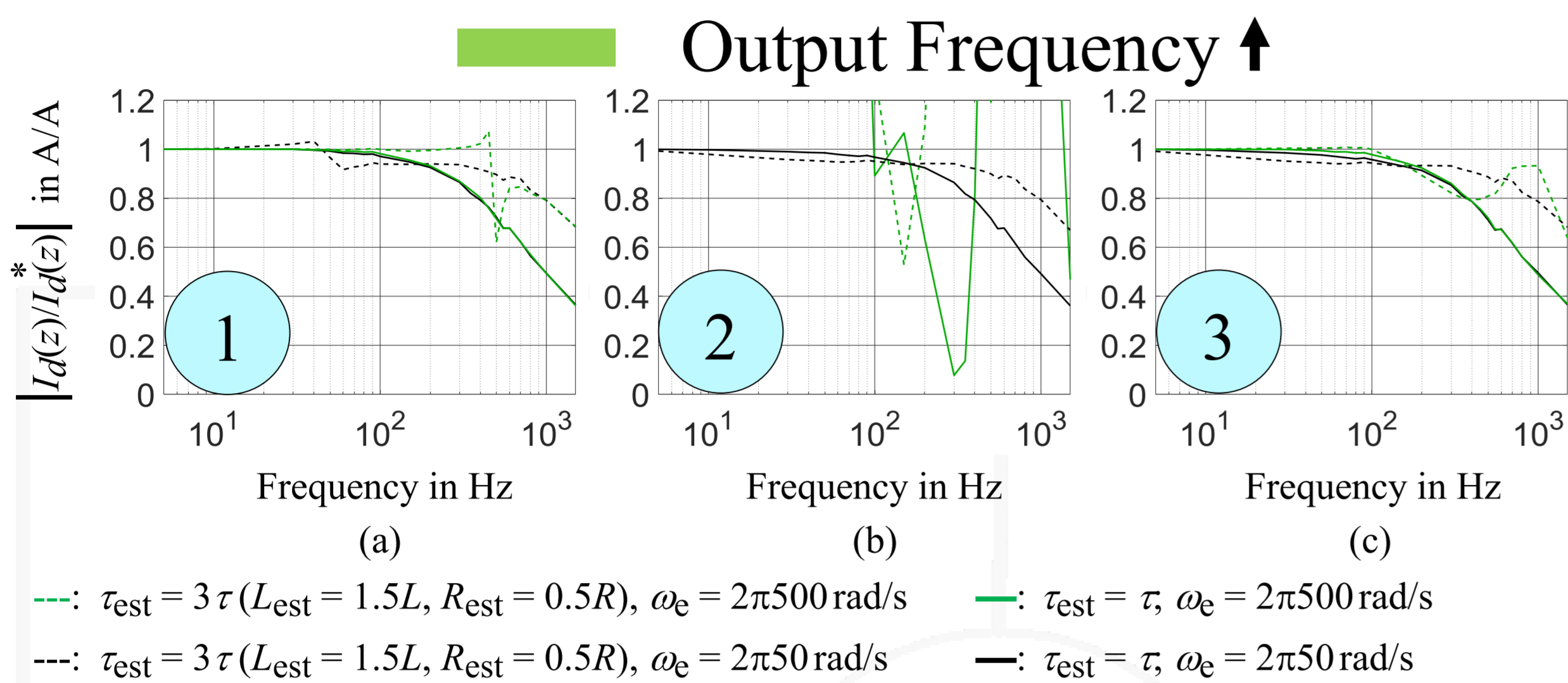


Discrete State Block Diagram of the *RL*-plant of a VSI with MICC (blue) and MID (yellow) and full DSFb (red).

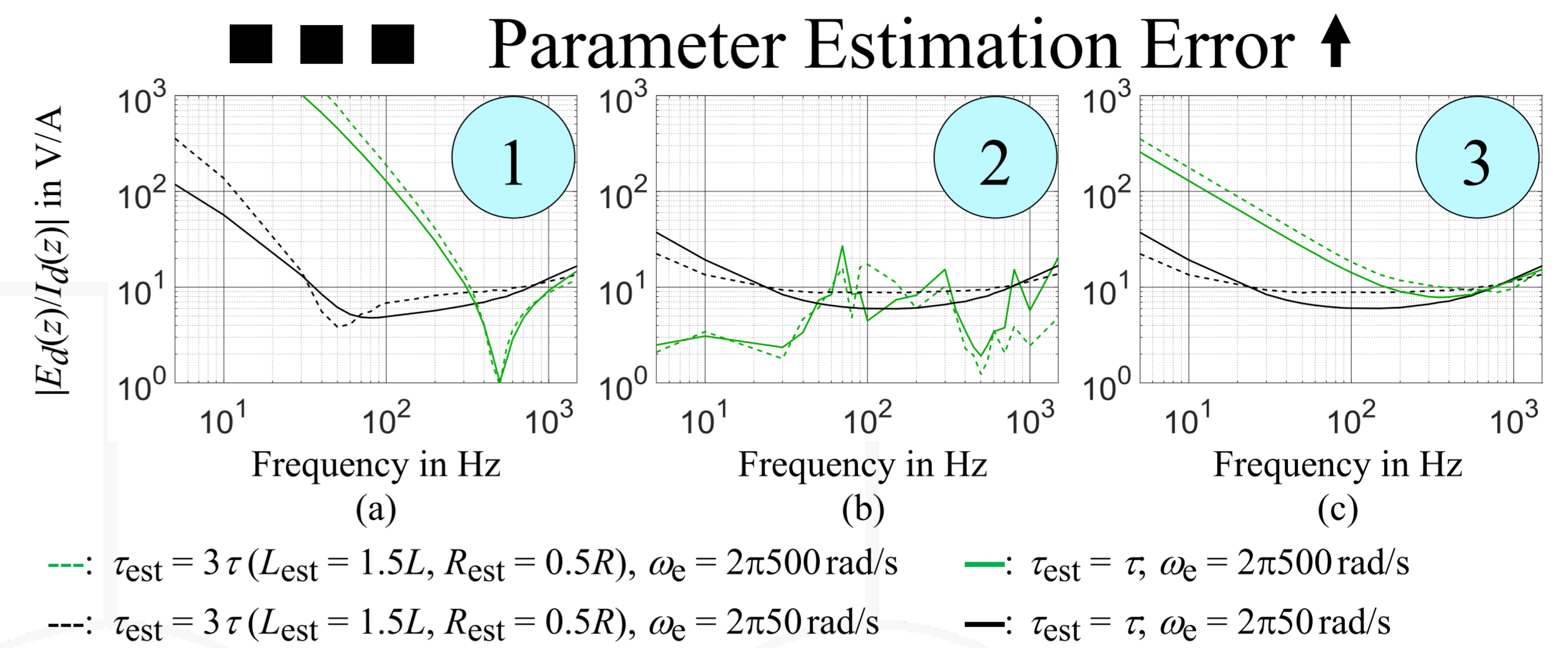
- Four different dq-decoupling techniques are presented and compared during dynamic events:
  1. Discrete CCVC
  2. Quasi-Continuous DCCSFb
  3. Discrete DCCSFb w/ MID
  4. Full DSFb



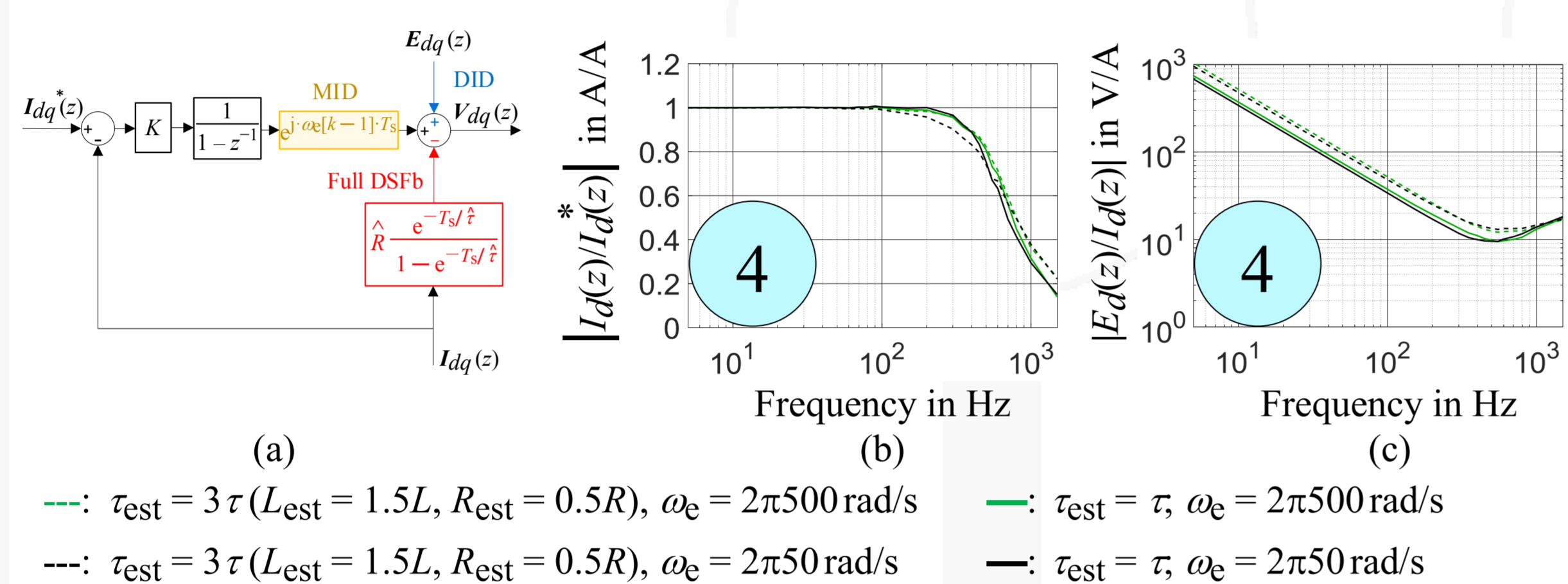
## Dynamic Analysis of the Control Techniques



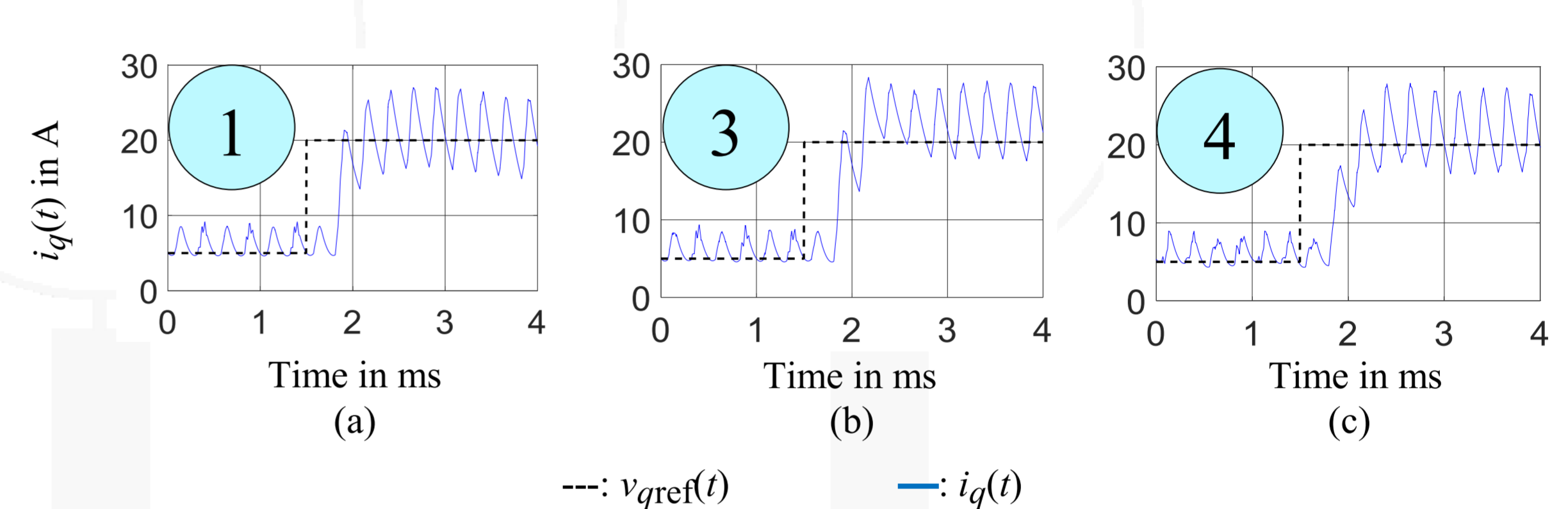
CT FRFs for 2kHz switching frequency, 500Hz bandwidth - (a) discrete CVC, (b) w/ continuous DCCSFb, (c) w/ discrete DCCSFb and MID.



DS FRFs for 2 kHz switching frequency, 500Hz bandwidth - (a) discrete CVC, (b) w/ continuous DCCSFb, (c) w/ discrete DCCSFb and MID.



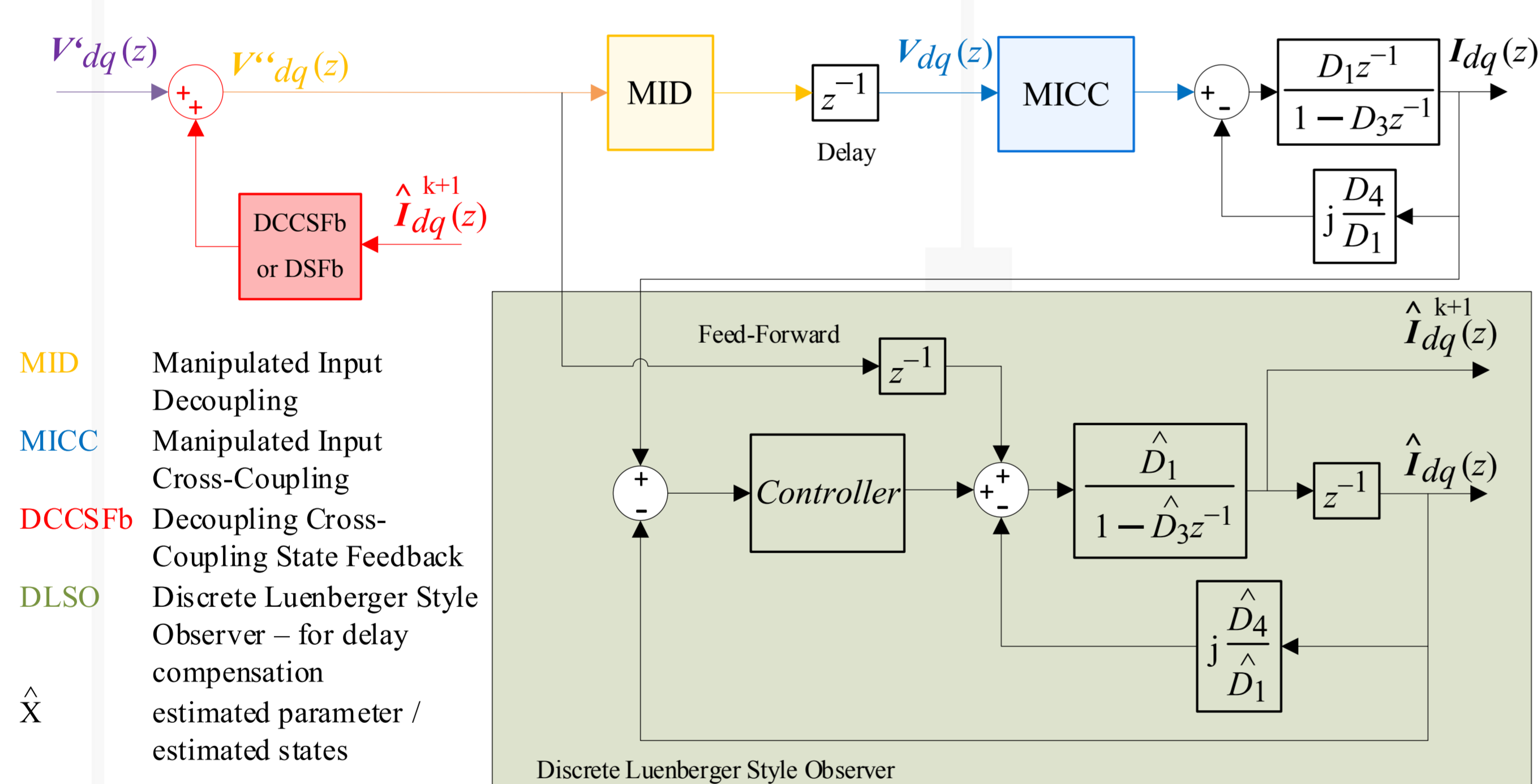
Control technique with full DSFb; (a) control structure, (b) CT, (c) DS @ 2 kHz switching frequency, 500 Hz bandwidth.



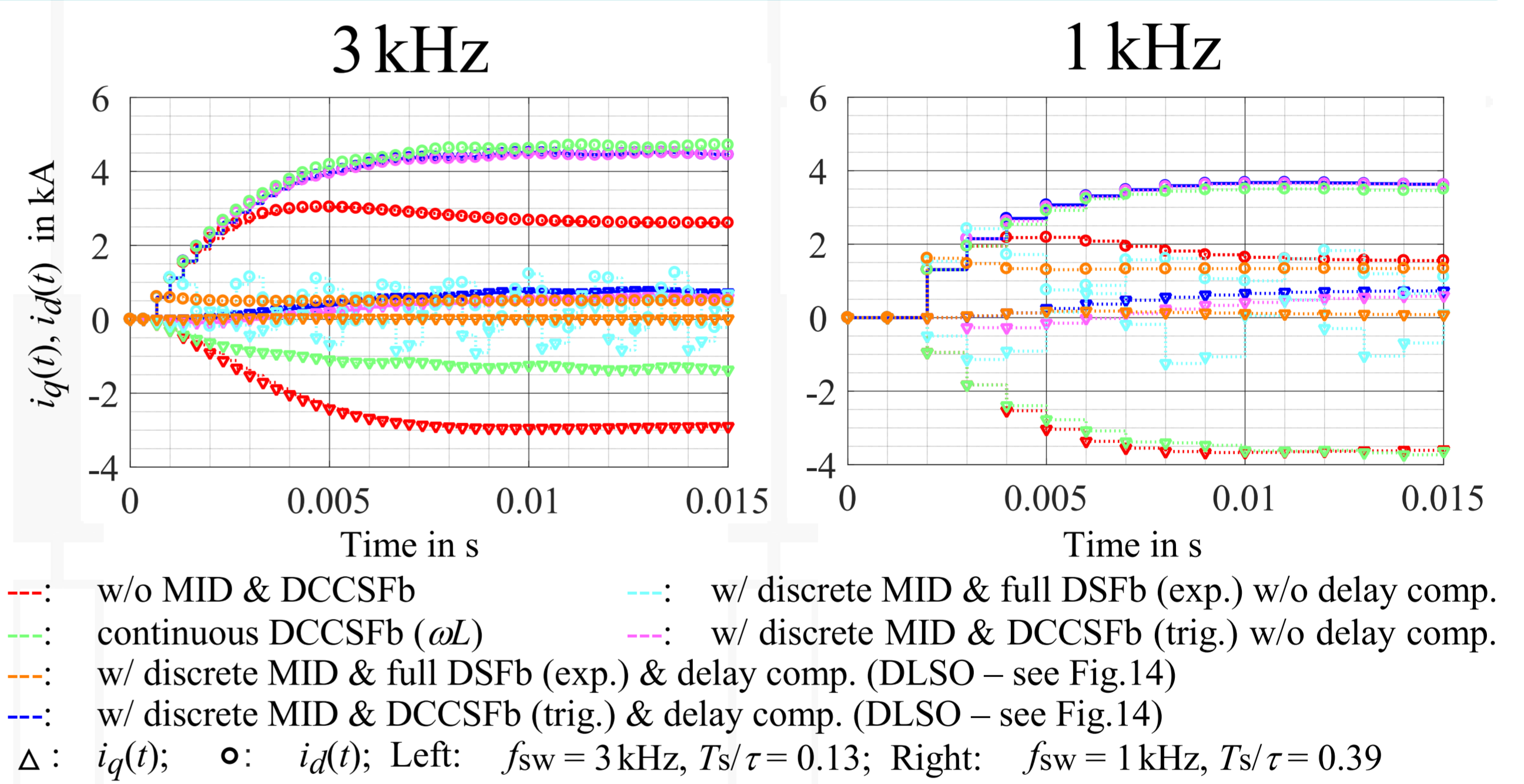
Response in  $q$ -axis current during a simultaneous step command on  $q$ - and  $d$ -axis current of the studied control topologies @  $\omega_e = 2\pi 500$  rad/s,  $f_{sw} = 2$  kHz and  $\tau_{est} = 3\tau$  ( $L_{est} = 1.5L, R_{est} = 0.5R$ ); (a) CVC, (b) discrete DCCSFb w/ MID, (c) discrete full DSFb w/ MID.

- Discrete approaches show superior command tracking and disturbance rejection properties
- The discrete techniques show different behavior regarding robustness
- The advantages of discrete approaches are more pronounced at lower switching frequencies

## Delay Compensation and Analysis of Different Decoupling Techniques (State-Feedback-based)



Discrete plant model with one period input delay with delay compensation using a Discrete Luenberger Style Observer (DLSO).



Open-loop step response of various MID & DCCSFb techniques with parameter estimation error for a 3 MW two-level inverter with 17s input delay.  $\tau_{est} = 1.5\tau$  ( $L_{est} = 1.2L, R_{est} = 0.8R$ ) Step command:  $v_{q,ref}(t) = 0$  V,  $v_{d,ref}(t) = 50$  V,  $\omega_e = 2\pi 50$  rad/s, w/ delayed parameters  $D_1-D_4$ .

- Discrete approaches provide enhanced decoupling properties compared to quasi-continuous (2)
- Technique (1) – high robustness, but can get oscillatory at high output frequencies
- Technique (3) – overall well-behaved. Medium sensitivity regarding delay and load estimation
- Technique (4) – well-behaved. Low parameter sensitivity regarding load but very sensitive to delay

## Conclusion

- At low switching frequencies (depends on ratio of  $T_s$  and  $\tau$ ), the advantages of discrete modeling become very apparent, especially at high synchronous speed (method 2 is unstable for some o.p.)
- The examined discrete decoupling techniques differ in robustness attributes